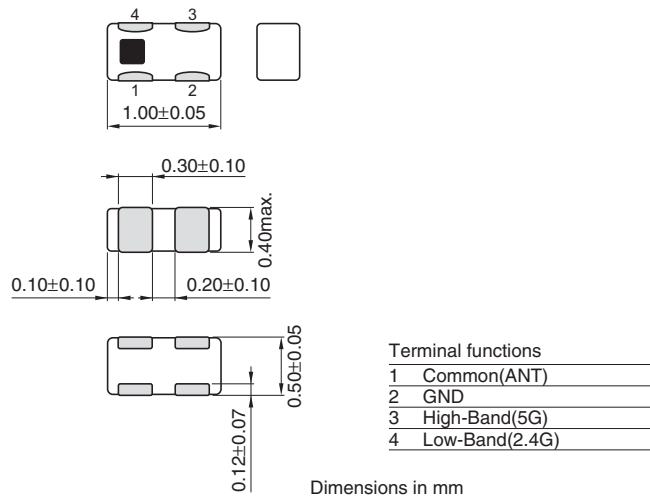


Multilayer Chip Diplexers For 2.4/5.0GHz W-LAN

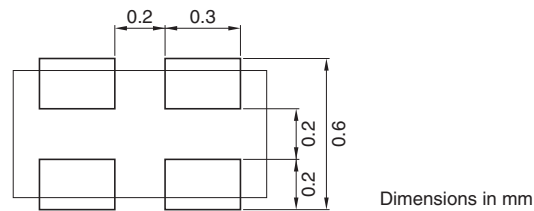
Conformity to RoHS Directive

DPX Series DPX105950DT-6010B1

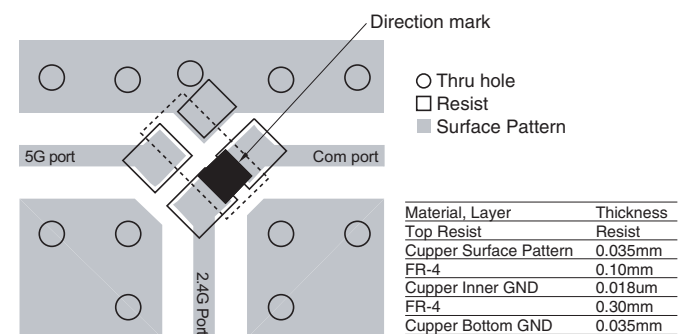
SHAPES AND DIMENSIONS



RECOMMENDED PCB BOARD PATTERNS



EVALUATION BOARD



Line width should be designed to match 50Ω characteristic impedance depending on PCB material and thickness.

- Conformity to RoHS Directive: This means that, in conformity with EU Directive 2002/95/EC, lead, cadmium, mercury, hexavalent chromium, and specific bromine-based flame retardants, PBB and PBDE, have not been used, except for exempted applications.

- All specifications are subject to change without notice.

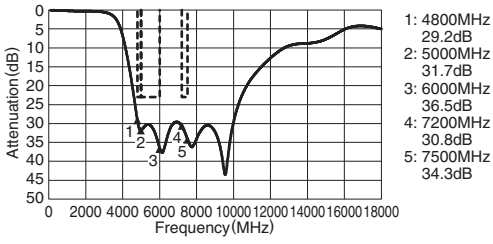
ELECTRICAL CHARACTERISTICS

Item		Frequency range		Minimum value	Typical value	Maximum value
Low-Band						
Insertion loss	[+25°C]	2400 to 2500MHz	(dB)	—	0.34	0.50
	[−40 to +85°C]	2400 to 2500MHz	(dB)	—	—	0.65
Attenuation		4800 to 6000MHz	(dB)	23	29	—
		7200 to 7500MHz	(dB)	23	30	—
Return loss		2400 to 2500MHz	(dB)	10	20	—
High-Band						
Insertion loss	[+25°C]	4900 to 5950MHz	(dB)	—	0.65	0.80
	[−40 to +85°C]	4900 to 5950MHz	(dB)	—	—	1.00
Attenuation		30 to 2700MHz	(dB)	23	25	—
		2400 to 2500MHz	(dB)	25	34	—
		9800 to 11900MHz	(dB)	20	25	—
Return loss		4900 to 5950MHz	(dB)	10	16	—
Common port						
Return loss		2400 to 2500MHz	(dB)	10	22	—
		4900 to 5950MHz	(dB)	10	16	—
Power Capacity			(dBm)	—	—	30
Temperature range		Operating	(°C)	−40	—	+85
		Storage	(°C)	−40	—	+85

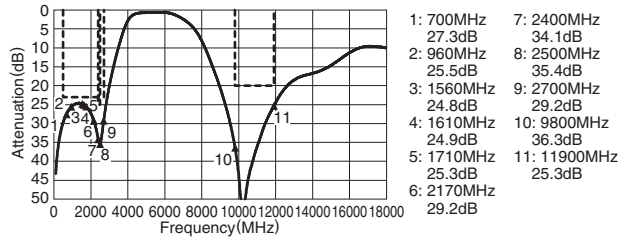
• Ta: +25°C

FREQUENCY CHARACTERISTICS

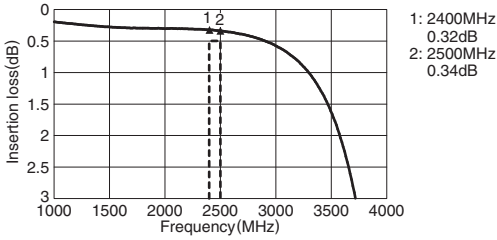
Low-BAND PORT ATTENUATION S21



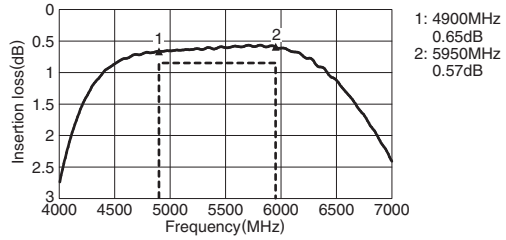
High-BAND PORT ATTENUATION S31



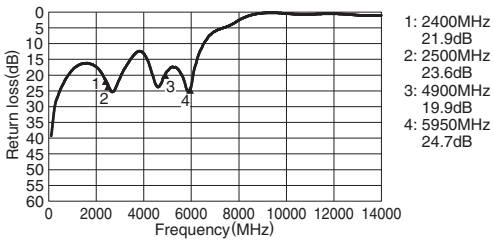
Low-BAND PORT INSERTION LOSS S21



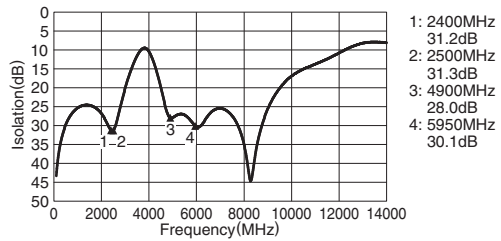
High-BAND PORT INSERTION LOSS S31



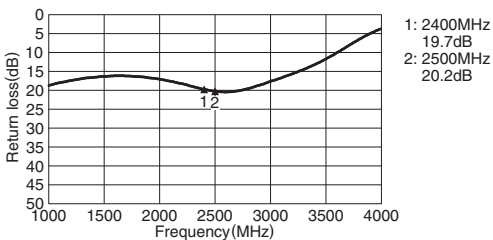
COMMON PORT RETURN LOSS S11



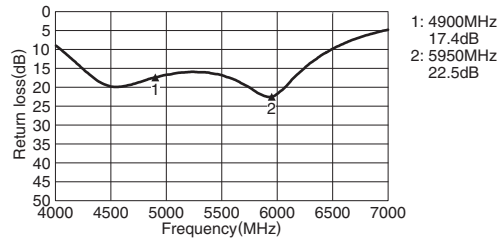
ISOLATION S23



Low-BAND PORT RETURN LOSS S22



High-BAND PORT RETURN LOSS S33



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